

30V P-Channel Enhancement Mode MOSFET

• General Description

AP4409 combines advanced MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is most suitable to load-switch or PWM applications.

• Applications

- DC-DC converter for portable devices
- Load switch
- Battery protection





• Product Summary

V_{DS}	-30V
In (at $V_{GS} = -10V$)	-15A
$R_{DS(ON)}$ (at $V_{GS} = -10V$)	< 7.5mΩ
$R_{DS(ON)}$ (at $V_{GS} = -4.5V$)	< 12mΩ

• Absolute Maximum Ratings Ta = 25°C

Parameter)	Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±20	V
Continuous Drain Current	Ta = 25°C	T_	-15	
	Ta = 70°C	I_D	-12.8	Δ
Pulsed Drain Current	I_{DM}	-80	A	
Avalanche Current	I _{AS} , I _{AR}	30		
Power Dissipation	Ta = 25°C	P_{D}	3.1	147
	Ta = 70°C	PD	2	W
Avalanche Energy (L = 0.1mH)		E _{AS} , E _{AR}	135	mJ
Thermal Resistance. Junction-to-Ambient	t ≤ 10s	$R_{ heta JA}$	40	
Thermal Resistance. Junction-to-Ambient	Steady State	ХθЈА	75	°C/W
Thermal Resistance. Junction-to-Lead	Steady State	$R_{ heta JL}$	24	
Operating Junction Temperature		T_{J}	150	°C
Storage Temperature Range	T_{STG}	-55 to 150	٠	



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• Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	I_D =-250 μ A, V_{GS} =0V	-30			V
Zero Gate Voltage Drain Current	,	V_{DS} =-30V, V_{GS} =0V			-5	
	I_{DSS}	V_{DS} =-30V, V_{GS} =0V, T_{J} =55°C			-25	μA
Gate-Body Leakage Current	I_{GSS}	V_{DS} =0V, V_{GS} =±20V		K	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-1.4	0.7	-2.7	V
	1	V _{GS} =-10V, I _D =-15A			7.5	
Static Drain-Source On-State Resistance	R _{DS(ON)}	V_{GS} =-10V, I_D =-15A, T_J =125°C			11.5	mΩ
		V_{GS} =-4.5V, I_{D} =-10A	43	1	12	
On-State Drain Current	$I_{D(ON)}$	V_{GS} =-10V, V_{DS} =-5V	-80			A
Forward Transconductance	g_{FS}	V_{DS} =-5V, I_D =-15A	35	50		S
Input Capacitance	C_{iss}			5270	6400	
Output Capacitance	Coss	V_{GS} =0V, V_{DS} =-15V, f =1MHz		945		pF
Reverse Transfer Capacitance	C_{rss}			745		
Gate Resistance	Rg	$V_{GS}=0V$, $V_{DS}=0V$, $f=1MHz$		2	3	Ω
Total Gate Charge (10V)				100	120	
Total Gate Charge (4.5V)	Q_{g}	V - 10V V - 15V I - 15A		51.5		, nC
Gate Source Charge	Q_{gs}	V_{GS} =-10V, V_{DS} =-15V, I_{D} =-15A		14.5		nC
Gate Drain Charge	Q_{gd}			23		
Turn-On Delay Time	t _{D(on)}			14		
Turn-On Rise Time	t _r	V_{GS} =-10V, V_{DS} =-15V, R_{L} =1 Ω ,		16.5		
Turn-Off Delay Time	$t_{\mathrm{D(off)}}$	$R_{GEN}=3\Omega$		76.5		ns
Turn-Off Fall Time	t_{f}			37.5		
Body Diode Reverse Recovery Time	t_{rr}	I_F =-15A, d_i/d_t =100A/ μ s		36.7	45]
Body Diode Reverse Recovery Charge	Q_{rr}	I_F =-15A, d_i/d_t =100A/ μ s		28		nC
Maximum Body-Diode Continuous Current	I_S				-5	A
Diode Forward Voltage	V_{SD}	I_S =-1A, V_{GS} =0V			-1	V

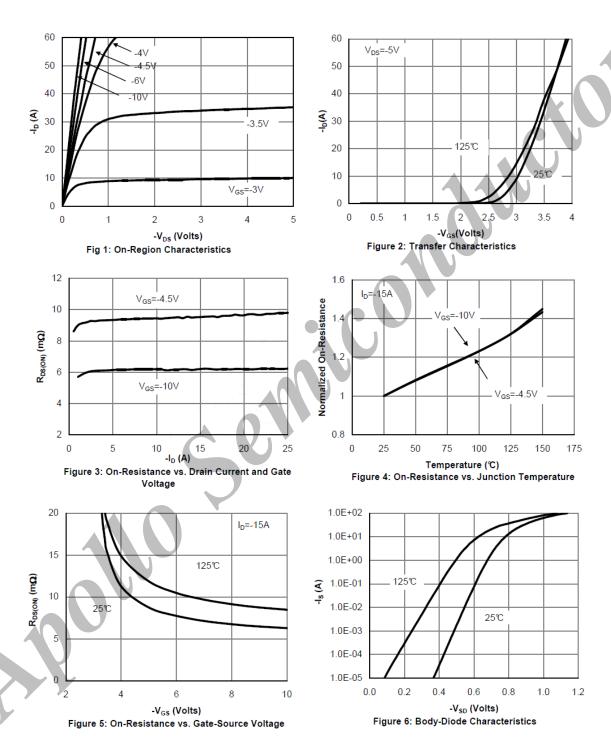
Ordering Information

Ordering Part Number	Package	MOQ
AP4409	SOP-8	2,500 pcs / reel

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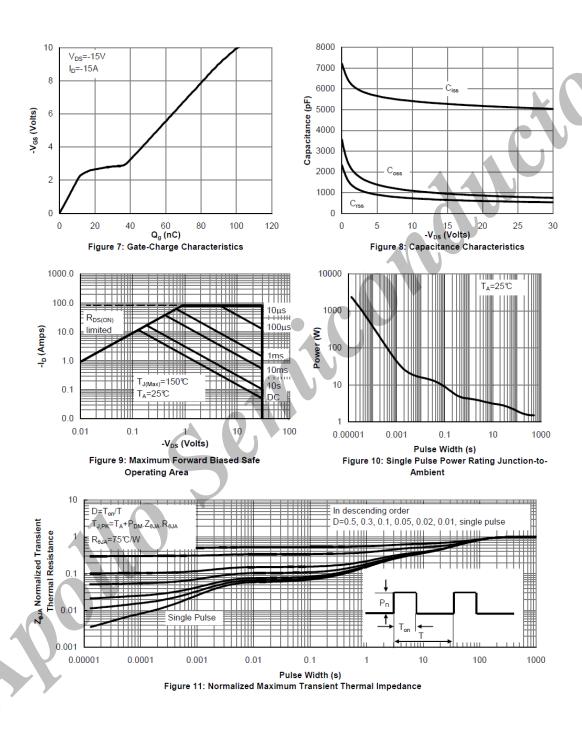
• Typical Electrical and Thermal Characteristics



Note 1: The static characteristics in Figure 1 to 6 are obtained using $<300\mu A$ pulses, duty cycle 0.5% max.



• Typical Electrical and Thermal Characteristics



Note 2: The curves in Figure 9 to 11 are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in^2 FR-4 board with 2oz. copper, assuming a maximum junction temperature of $T_{\text{J(MAX)}}$ =150°C. The SOA curve provides a single pulse rating.



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