

20V P-Channel Enhancement Mode MOSFET

• General Description

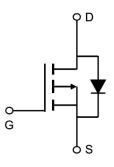
AP3419A combines advanced MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is most suitable to load-switch or PWM applications.

• Applications

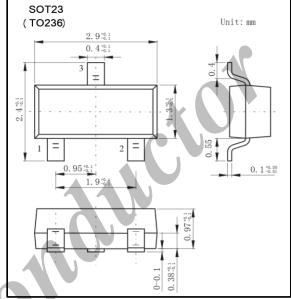
- DC/DC converter for portable devices
- Load switch

Product Summary

$V_{ extsf{DS}}$	-20V
In (at $V_{GS} = -10V$)	-3.5A
$R_{DS(ON)}$ (at $V_{GS} = -10V$)	$<75 \mathrm{m}\Omega$
$R_{DS(ON)}$ (at $V_{GS} = -4.5V$)	< 95mΩ
$R_{DS(ON)}$ (at $V_{GS} = -2.5V$)	< 145mΩ











• Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	-20	V	
Gate-Source Voltage	V_{GS}	±12	V	
Continuous Drain Current TA=25°C		-3.5	А	
T _A =70°C	I _D	-2.8		
Pulsed Drain Current *	I _{DM}	-15		
Power Dissipation T _A = 25°C	P _D	1.4	W	
T _A = 70°C		0.9	VV	
Thermal Resistance. Junction-to-Ambient t≤10s	D	90	°C/W	
Thermal Resistance. Junction-to-Ambient (Stead-state)	$R_{ heta JA}$	125		
Thermal Resistance. Junction-to-Case (Stead-state)	$R_{ heta JC}$	60		
Junction Temperature	ΤJ	150	°C	
Storage Temperature Range	Тsтg	-55 to 150	L C	

^{*} Repetitive rating, pulse width limited by junction temperature $T_{J(MAX)}=150$ °C. Ratings are based on low frequency and duty cycles to keep initial $T_J=25$ °C.

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• Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Drain-Source Breakdown Voltage	V_{DSS}	$I_D = -250 \mu A$, $V_{GS} = 0 V$	-20			V	
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-16V, V_{GS} =0V			-0.5		
		V_{DS} =-16V, V_{GS} =0V, T_{J} =55°C			-2.5	μA	
Gate-Body leakage current	I_{GSS}	V_{DS} =0V, V_{GS} =±10V			±1	μА	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-250\mu A$	-0.7	-0.9	-1.4	V	
Static Drain-Source On-Resistance	R _{DS(ON)}	V_{GS} =-10V, I_{D} =-3.5A		59	75		
		V_{GS} =-10V, I_{D} =-3.5A T_{J} =125°C		83	105		
		V_{GS} =-4.5V, I_{D} =-3A		76	95	mΩ	
		V_{GS} =-2.5V, I_{D} =-1A		111	145		
On state drain current	$I_{D(ON)}$	V_{GS} =-4.5V, V_{DS} =-5V	-15			A	
Forward Transconductance	$\mathbf{g}_{ ext{FS}}$	V_{DS} =-5V, I_{D} =-3.5A		6.8		S	
Input Capacitance	C_{iss}			512	620		
Output Capacitance	C_{oss}	V_{GS} =0V, V_{DS} =-10V, f=1MHz		77		pF	
Reverse Transfer Capacitance	C_{rss}			62			
Gate Resistance	R_{g}	V_{GS} =0V, V_{DS} =0V, f =1MHz		9.2	13	Ω	
Total Gate Charge	Q_{g}			5.5	6.6		
Gate Source Charge	Q_{gs}	V_{GS} =-4.5V, V_{DS} =-10V, I_{D} =-3.5A		8.0		nC	
Gate Drain Charge	Q_{gd}			1.9			
Turn-On Delay Time	t _{D(on)}			5			
Turn-On Rise Time	$t_{\rm r}$	V_{GS} =-10V, V_{DS} =-10V,		6.7			
Turn-Off Delay Time	$t_{ m D(off)}$	$R_L=2.8\Omega$, $R_{GEN}=3\Omega$		28		ns	
Turn-Off Fall Time	t_{f}			13.5			
Body Diode Reverse Recovery Time	t _{rr}	I_F =-3.5A, d_I/d_t =100A/ μ s		9.8	12		
Body Diode Reverse Recovery Charge	$Q_{\rm rr}$	I_F =-3.5A, d_I/d_t =100A/ μs		2.7		nC	
Maximum Body-Diode Continuous Current	I_S				-2	A	
Diode Forward Voltage	V_{SD}	I_S =-1A, V_{GS} =0V	-0.65	-0.81	-0.95	V	

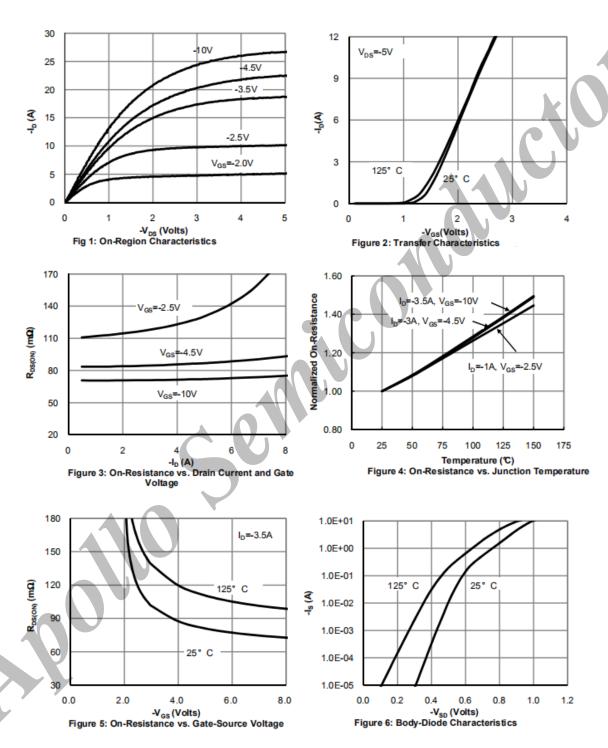
Ordering Information

Ordering Part Number	Package	MOQ
AP3419A	SOT23 (TO236)	3,000 pcs / reel

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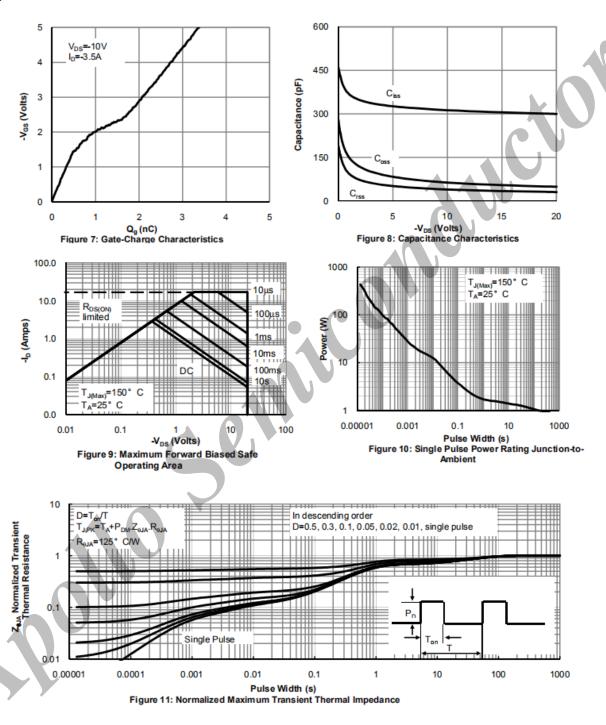
• Typical Characteristics



The static characteristics in Figures 1 to 6 are obtained using <300µs pulses, duty cycle 0.5% max.



Typical Characteristics



These curves in Figures 9 to 11 are based on the junction-to-ambient thermal impedance which is measured with the device mounted on 1in^2 FR-4 board with 2oz. Copper, assuming a maximum junction temperature of $T_{J(MAX)}$ =150°C. The SOA curve provides a single pulse rating.



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