

AP3414B 20V N-Channel Enhancement Mode MOSFET

• General Description

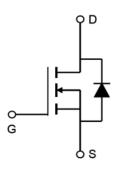
AP3414B combines advanced MOSFET technology with a low resistance package to provide extremely low $R_{DS(\text{ON})}$. This device is most suitable to load-switch or PWM applications.

Applications

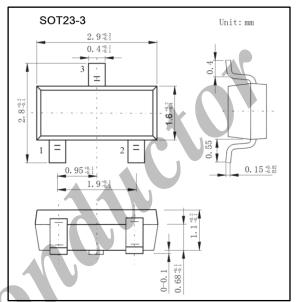
- DC/DC converter for portable devices
- Load switch

Product Summary

$V_{ extsf{DS}}$	20V
In (at $V_{GS} = 4.5V$)	4.2A
$R_{DS(ON)}$ (at $V_{GS} = 4.5V$)	< 50mΩ
$R_{DS(ON)}$ (at $V_{GS} = 2.5V$)	< 63mΩ
$R_{DS(ON)}$ (at $V_{GS} = 1.8V$)	< 87mΩ











Absolute Maximum Ratings Ta = 25°C

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	20	V
Gate-Source Voltage		V_{GS}	±8	V
Continuous Drain Current	T _A =25°C		4.2	
	T _A =70°C	I _D	3.2	Α
Pulsed Drain Current *		I _{DM}	15	
Power Dissipation	T _A =25°C	D	1.4	W
	T _A =70°C	P_{D}	0.9	VV
Thermal Resistance. Junction	n- to-Ambient	$R_{ heta JA}$	125	°C/W
Thermal Resistance. Junction	n- to-Case	$R_{ heta JC}$	80	°C/W
Junction and Storage Tempe	rature Range	Тı, Тsтg	-55 to 150	°C

 $[\]ensuremath{^{*}}$ Repetitive rating, pulse width limited by junction temperature.



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• Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Drain-Source Breakdown Voltage	V_{DSS}	$I_D = 250 \mu A$, $V_{GS} = 0 V$	20			V	
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS}=16V$, $V_{GS}=0V$			1		
		V _{DS} =16V, V _{GS} =0V, T _J =55°C			5	μΑ	
Gate-Body leakage current	I_{GSS}	V_{DS} =0V, V_{GS} =±8V			±100	nA	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	0.4	0.6	1	V	
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =4.5V, I _D =4.2A		41	50	mΩ	
		V_{GS} =4.5V, I_{D} =4.2A T_{J} =125°C		58	70		
		V _{GS} =2.5V, I _D =3.7A		52	63	mΩ	
		V _{GS} =1.8V, I _D =3.2A		67	87	mΩ	
On state drain current	$I_{D(ON)}$	V _{GS} =4.5V, V _{DS} =5V	15			A	
Forward Transconductance	$\mathbf{g}_{ extsf{FS}}$	V _{DS} =5V, I _D =4.2A		11		S	
Input Capacitance	C_{iss}			436		pF	
Output Capacitance	C_{oss}	V_{GS} =0V, V_{DS} =10V, f=1MHz		66		pF	
Reverse Transfer Capacitance	C_{rss}			44		pF	
Gate Resistance	R_{g}	$V_{GS}=0V$, $V_{DS}=0V$, $f=1MHz$		3		Ω	
Total Gate Charge	Q_{g}			6.2		nC	
Gate Source Charge	Q_{gs}	V_{GS} =4.5V, V_{DS} =10V, I_{D} =4.2A		1.6		nC	
Gate Drain Charge	Q_{gd}			0.5		nC	
Turn-On Delay Time	$t_{D(on)}$			5.5		ns	
Turn-On Rise Time	$t_{\rm r}$	V_{GS} =4.5V, V_{DS} =10V,		6.3		ns	
Turn-Off Delay Time	$t_{ m D(off)}$	R_L =2.7 Ω , R_{GEN} =6 Ω		40		ns	
Turn-Off Fall Time	t_{f}			12.7		ns	
Body Diode Reverse Recovery Time	t_{rr}	I_F =4A, d_I/d_t =100A/ μ s		12.3		ns	
Body Diode Reverse Recovery Charge	Q_{rr}	I_F =4A, d_I/d_t =100A/ μ s		3.5		nC	
Maximum Body-Diode Continuous Current	I_S				2	A	
Diode Forward Voltage	V_{SD}	$I_S=1A$, $V_{GS}=0V$		0.76	1	V	

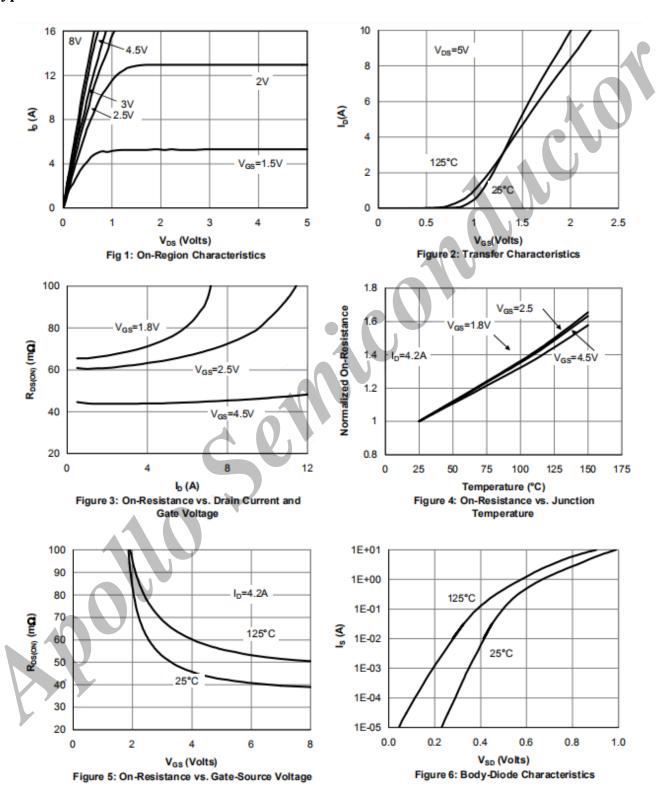
Ordering Information

Ordering Part Number	Package	MOQ
AP3414B	SOT23-3	3,000 pcs / reel

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• Typical Characteristics





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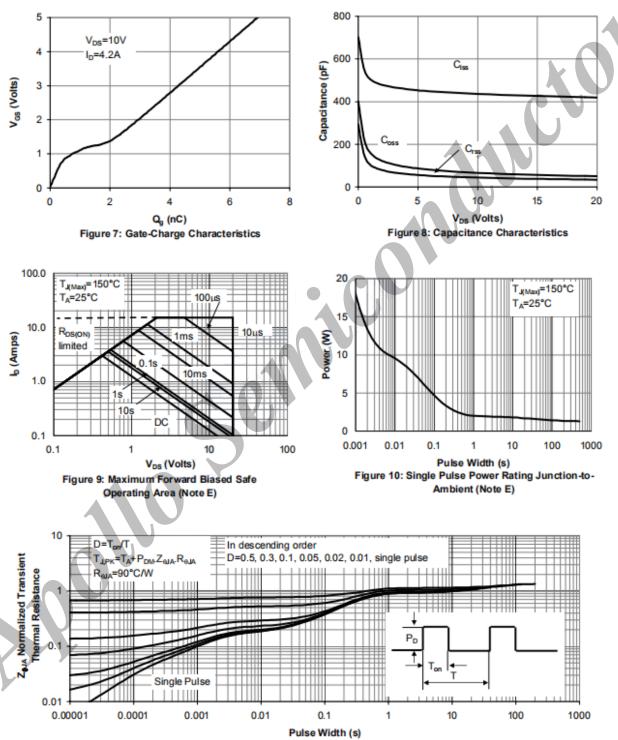


Figure 11: Normalized Maximum Transient Thermal Impedance

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