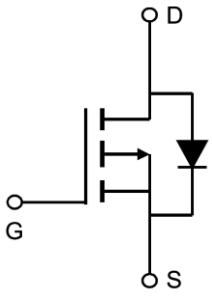
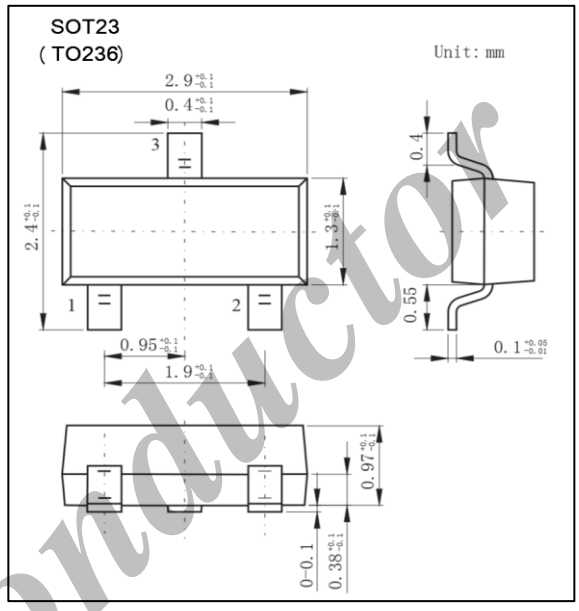
AP3401A
30V P-Channel Enhancement Mode MOSFET

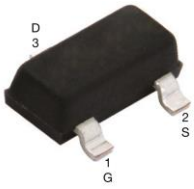
- **General Description**

AP3401A combines advanced MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$. This device is most suitable to load-switch or PWM applications.





Top View



- **Applications**

- DC/DC converter for portable devices
- Load switch

- **Product Summary**

V_{DS}	-30V
I_D (at V_{GS} = -10V)	-4.2A
$R_{DS(ON)}$ (at V_{GS} = -10V)	< 50mΩ
$R_{DS(ON)}$ (at V_{GS} = -4.5V)	< 65mΩ
$R_{DS(ON)}$ (at V_{GS} = -2.5V)	< 120mΩ

- **Absolute Maximum Ratings Ta = 25°C**

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		V_{DS}	-30	V
Gate-Source Voltage		V_{GS}	±12	V
Continuous Drain Current	T_A=25°C	I_D	-4.2	A
	T_A=70°C		-3.5	
Pulsed Drain Current *		I_{DM}	-30	
Power Dissipation	T_A= 25°C	P_D	1.4	W
	T_A= 70°C		1	
Thermal Resistance. Junction-to-Ambient	t ≤ 10s	$R_{\theta JA}$	90	°C/W
Thermal Resistance. Junction-to-Ambient			125	
Thermal Resistance. Junction-to-Case		$R_{\theta JC}$	60	
Junction Temperature		T_J	150	°C
Junction and Storage Temperature Range		T_{STG}	-55 to 150	

* Repetitive rating, pulse width limited by junction temperature.

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- **Electrical Characteristics Ta = 25°C**

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V_{DSS}	I_D=-250μA, V_{GS}=0V	-30			V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=-24V, V_{GS}=0V			-1	μA
		V_{DS}=-24V, V_{GS}=0V, T_J=55°C			-5	
Gate-Body leakage current	I_{GSS}	V_{DS}=0V, V_{GS}=±12V			±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=-250μA	-0.4		-1.3	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=-10V, I_D=-4.2A			50	mΩ
		V_{GS}=-10V, I_D=-4.2A T_J=125°C			75	
		V_{GS}=-4.5V, I_D=-4A			65	
		V_{GS}=-2.5V, I_D=-1A			120	
On state drain current	$I_{D(ON)}$	V_{GS}=-4.5V, V_{DS}=-5V	-25			A
Forward Transconductance	g_{FS}	V_{DS}=-5V, I_D=-5A	7	11		S
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=-15V, f=1MHz		954		pF
Output Capacitance	C_{oss}			115		
Reverse Transfer Capacitance	C_{rss}			77		
Gate Resistance	R_g	V_{GS}=0V, V_{DS}=0V, f=1MHz		6		Ω
Total Gate Charge	Q_g	V_{GS}=-4.5V, V_{DS}=-15V, I_D=-4A		9.4		nC
Gate Source Charge	Q_{gs}			2		
Gate Drain Charge	Q_{gd}			3		
Turn-On Delay Time	$t_{D(on)}$	V_{GS}=-10V, V_{DS}=-15V, R_L=3.6Ω, R_{GEN}=6Ω		6.3		ns
Turn-On Rise Time	t_r			3.2		
Turn-Off Delay Time	$t_{D(off)}$			38.3		
Turn-Off Fall Time	t_f			12		
Body Diode Reverse Recovery Time	t_{rr}	I_F=-4A, d_I/d_t=100A/ μs		20.2		
Body Diode Reverse Recovery Charge	Q_{rr}	I_F=-4A, d_I/d_t=100A/ μs		11.2		nC
Maximum Body-Diode Continuous Current	I_S				-2.2	A
Diode Forward Voltage	V_{SD}	I_S=-1A, V_{GS}=0V		-0.75	-1	V

- **Ordering Information**

Ordering Part Number	Package	MOQ
AP3401A	SOT23 (TO236)	3,000 pcs / reel

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- **Typical Characteristics**

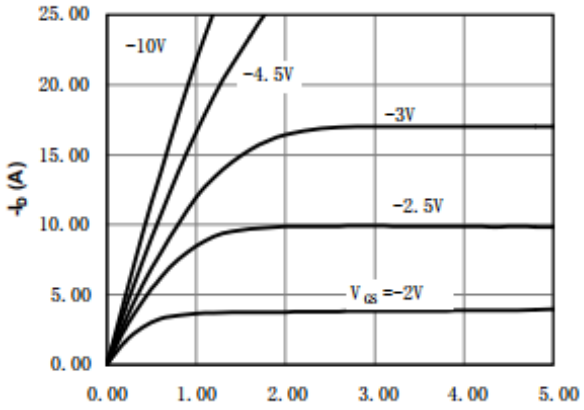

Fig 1: On-Region Characteristics

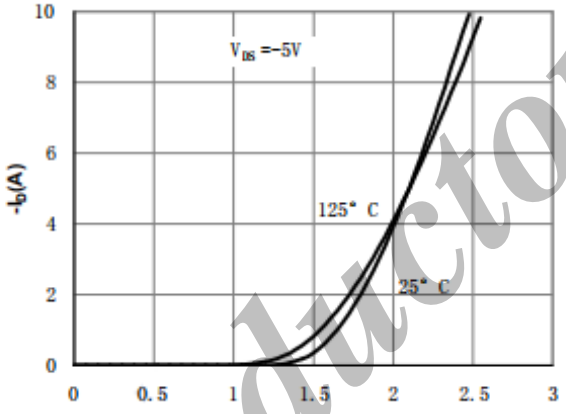

Figure 2: Transfer Characteristics

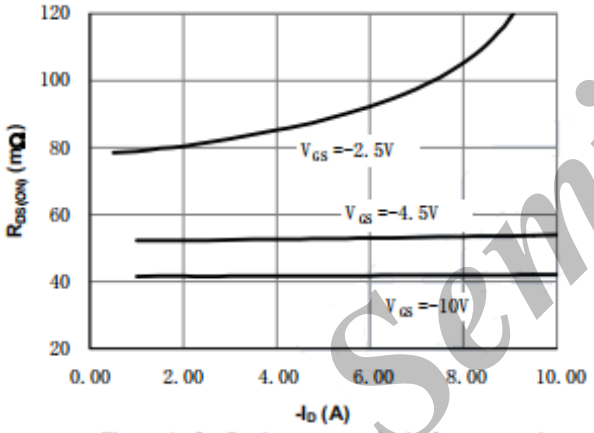

Figure 3: On-Resistance vs. Drain Current and Gate Voltage

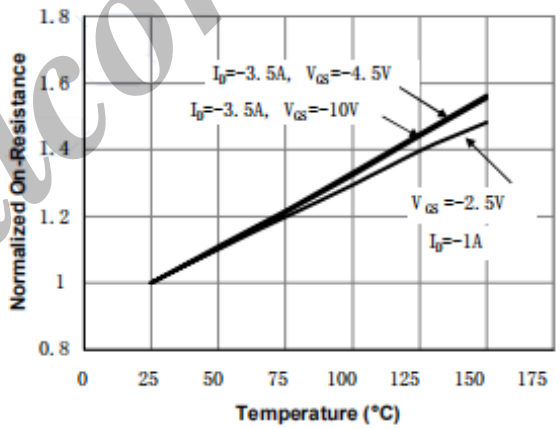

Figure 4: On-Resistance vs. Junction Temperature

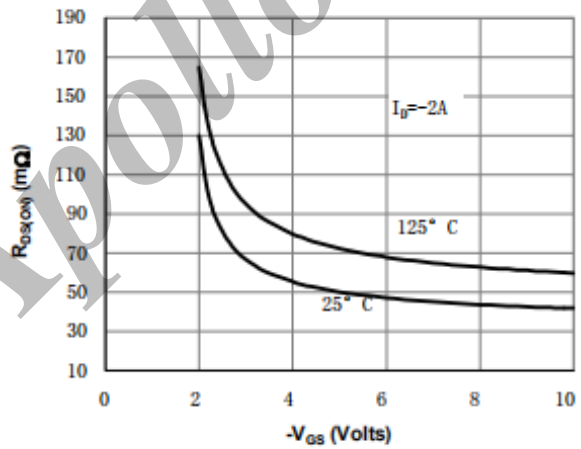

Figure 5: On-Resistance vs. Gate-Source Voltage

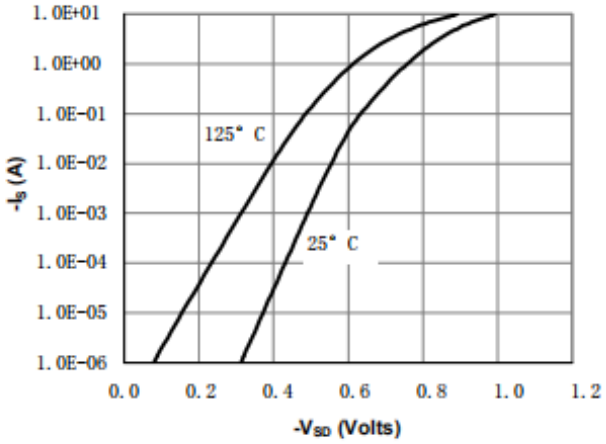

Figure 6: Body-Diode Characteristics

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- **Typical Characteristics**

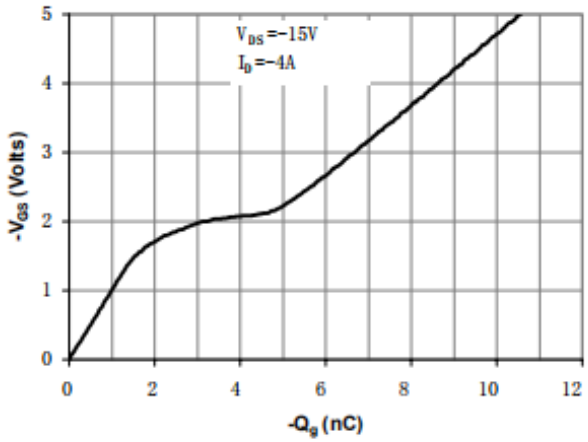

Figure 7: Gate-Charge Characteristics

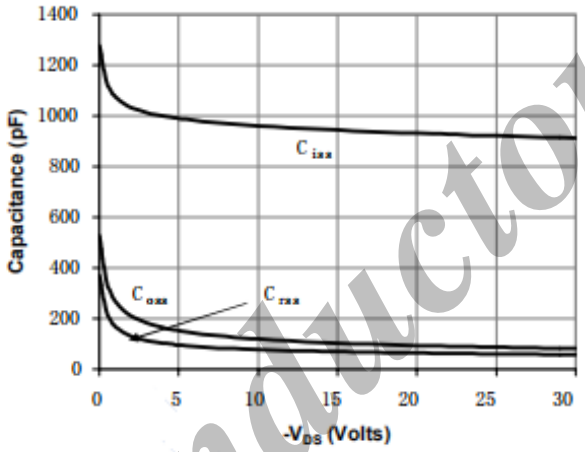

Figure 8: Capacitance Characteristics

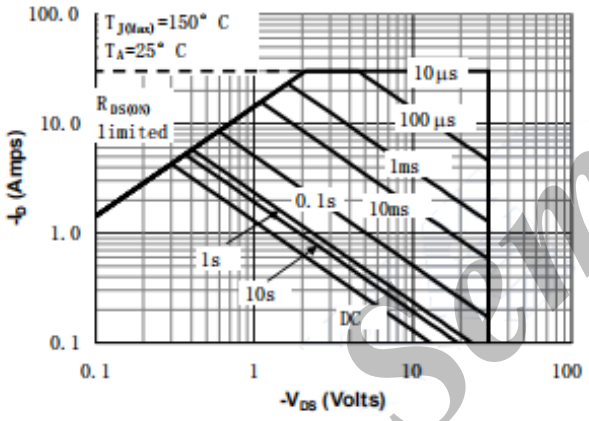

Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

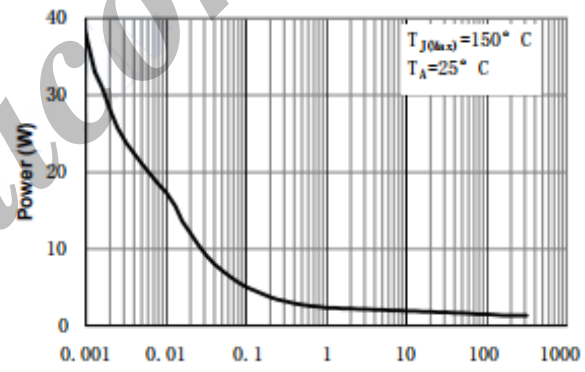

Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

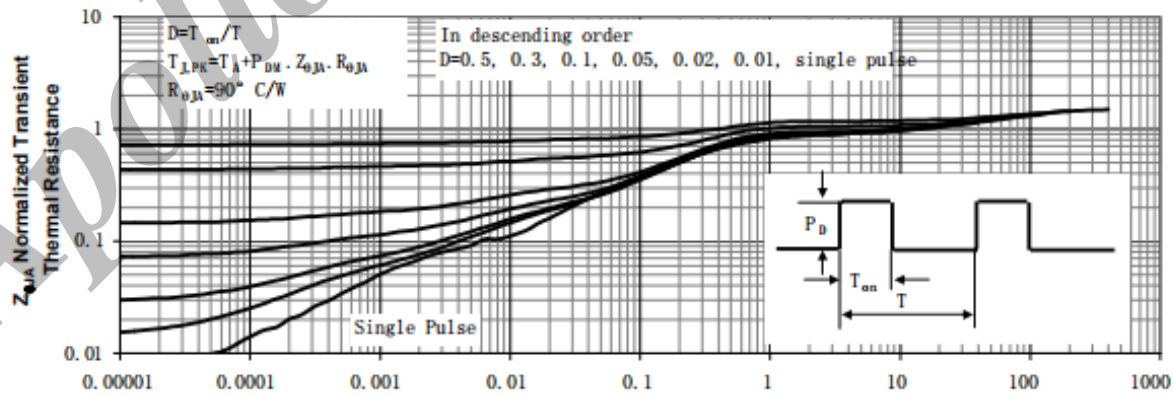

Figure 11: Normalized Maximum Transient Thermal Impedance

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