

# AP2306A 30V N-Channel Enhancement Mode MOSFET

## • General Description

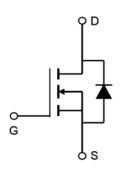
AP2306A combines advanced MOSFET technology with a low resistance package to provide extremely low  $R_{DS(\text{ON})}$ . This device is most suitable to load-switch or PWM applications.

#### Applications

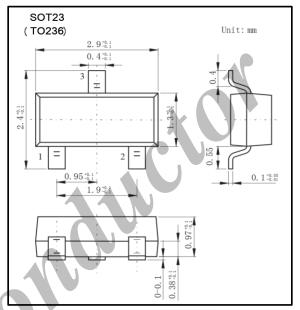
- DC/DC converter for portable devices
- Load switch

#### Product Summary

 $\begin{array}{ll} V_{DS} & 30V \\ R_{DS(ON)} \mbox{ (at $V_{GS}=10$V, $I_D=3.5$A)} & <57 m\Omega \\ R_{DS(ON)} \mbox{ (at $V_{GS}=4.5$V, $I_D=2.8$A)} & <94 m\Omega \end{array}$ 











### Absolute Maximum Ratings (Ta = 25°C unless noted)

Parameter	Symbol	Rating	Unit		
Drain-Source Voltage	$V_{DS}$	30	V		
Gate-Source Voltage	$V_{GS}$	±20	V		
Continuous Drain Current *b	$I_{D}$ (Ta = 25°C)	3.5			
Continuous Drain Current	$I_{D}$ (Ta = 70°C)	2.8	A		
Pulsed Drain Current *a	I <sub>DM</sub>	16	А		
Continuous Source Current (Diode Conduction) *b	$I_{S}$	1.25			
Power Dissipation *b	$P_{D}$ (Ta = 25°C)	1.25	W		
rower dissipation	$P_{D}$ (Ta = 70°C)	0.8			
Thermal Resistance. Junction-to-Ambient	$R_{\theta JA} (t \le 5s)^{*b}$	100	°C/W		
	R <sub>θJA</sub> (Steady State) *c	130			
Junction Temperature	$T_{J}$	150	°C		
Storage Temperature Range	$T_{STG}$	-55 to 150			

#### Notes

<sup>\*</sup>a Pulse width limited by maximum junction temperature

<sup>\*</sup>b Surface Mounted on FR4 Board, t ≤ 5s.

<sup>\*</sup>c Surface Mounted on FR4 Board.

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#### • Electrical Characteristics (Ta = 25°C unless noted)

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit	
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D = 250 \mu A$ , $V_{GS} = 0 V$	30			V	
Zono Coto Voltago Duoin Comunit	,	$V_{DS}$ =30V, $V_{GS}$ =0V			0.5		
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS}$ =30V, $V_{GS}$ =0V, $T_{J}$ =55°C			10	μA	
Gate-Body Leakage Current	$I_{GSS}$	$V_{DS}$ =0V, $V_{GS}$ =±20V			±100	nA	
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}$ = $V_{GS}$ , $I_D$ =250 $\mu A$	1			V	
On-state Drain Current		$V_{DS} \ge 4.5V$ , $V_{GS} = 10V$	6			A	
	$I_{D(ON)}$	V <sub>DS</sub> ≥ 4.5V, V <sub>GS</sub> =4.5V	4				
Chatia Duain Causas On Desigtanas	Ъ	V <sub>GS</sub> =10V, I <sub>D</sub> =3.5A	7	46	57	mΩ	
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V <sub>GS</sub> =4.5V, I <sub>D</sub> =2.8A		70	94		
Forward Transconductance	$\mathbf{g}_{ ext{FS}}$	V <sub>DS</sub> =4.5V, I <sub>D</sub> =3.5A		6.9		S	
Diode Forward Voltage	$V_{SD}$	$I_S$ =1.25A, $V_{GS}$ =0V		08	1.2	V	
Input Capacitance *d	$C_{iss}$			555		pF	
Output Capacitance *d	$C_{\mathrm{oss}}$	$V_{GS}$ =0V, $V_{DS}$ =15V, f=1MHz		120			
Reverse Transfer Capacitance *d	$C_{rss}$			60			
Gate Charge *d	$Q_g$	$V_{GS}$ =5V, $V_{DS}$ =15V, $I_{D}$ =3.5A		4.2	7		
Total Gate Charge *d	$Q_{\mathrm{gt}}$			8.5	20	nC	
Gate Source Charge *d	$Q_{gs}$	$V_{GS}$ =10V, $V_{DS}$ =15V, $I_{D}$ =3.5A		1.9		IIC .	
Gate Drain Charge *d	$Q_{\mathrm{gd}}$			1.35			
Gate Resistance *d	$R_{g}$		0.5		2.4	Ω	
Turn-On Delay Time	t <sub>D(on)</sub>			9	20		
Turn-On Rise Time	$t_r$	$V_{GEN}$ =10V, $V_{DD}$ =15V, $I_{D}$ =1A,		7.5	18	nc	
Turn-Off Delay Time	$t_{D(off)}$	$R_L=15\Omega$ , $R_{GEN}=6\Omega$		17	35	ns	
Turn-Off Fall Time	$t_{\mathrm{f}}$			5.2	12		

Note

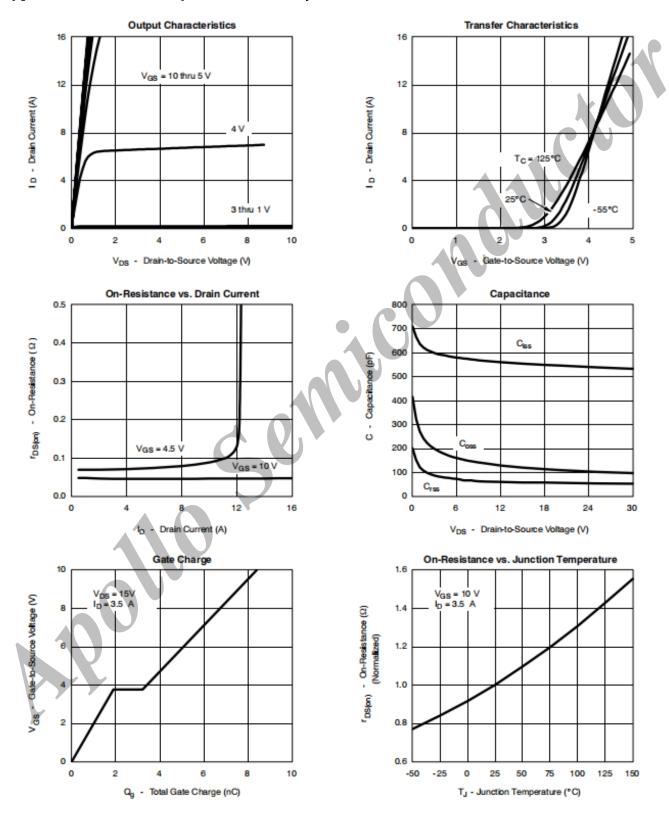
### • Ordering Information

	Ordering Part Number	Package	MOQ
ĺ	AP2306A	SOT23 (TO236)	3,000 pcs / reel

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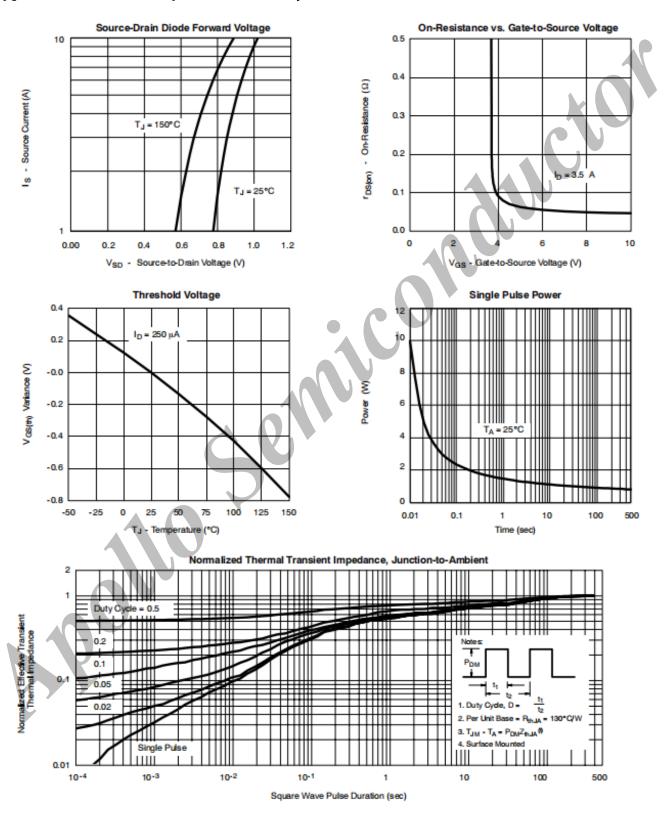
<sup>\*</sup>d Pulse Test: Pulse Width  $\leq 300\mu$ s, Duty Cycle  $\leq 2\%$ 

#### • Typical Characteristics (25°C unless noted)





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